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Figure 1: Development Flow

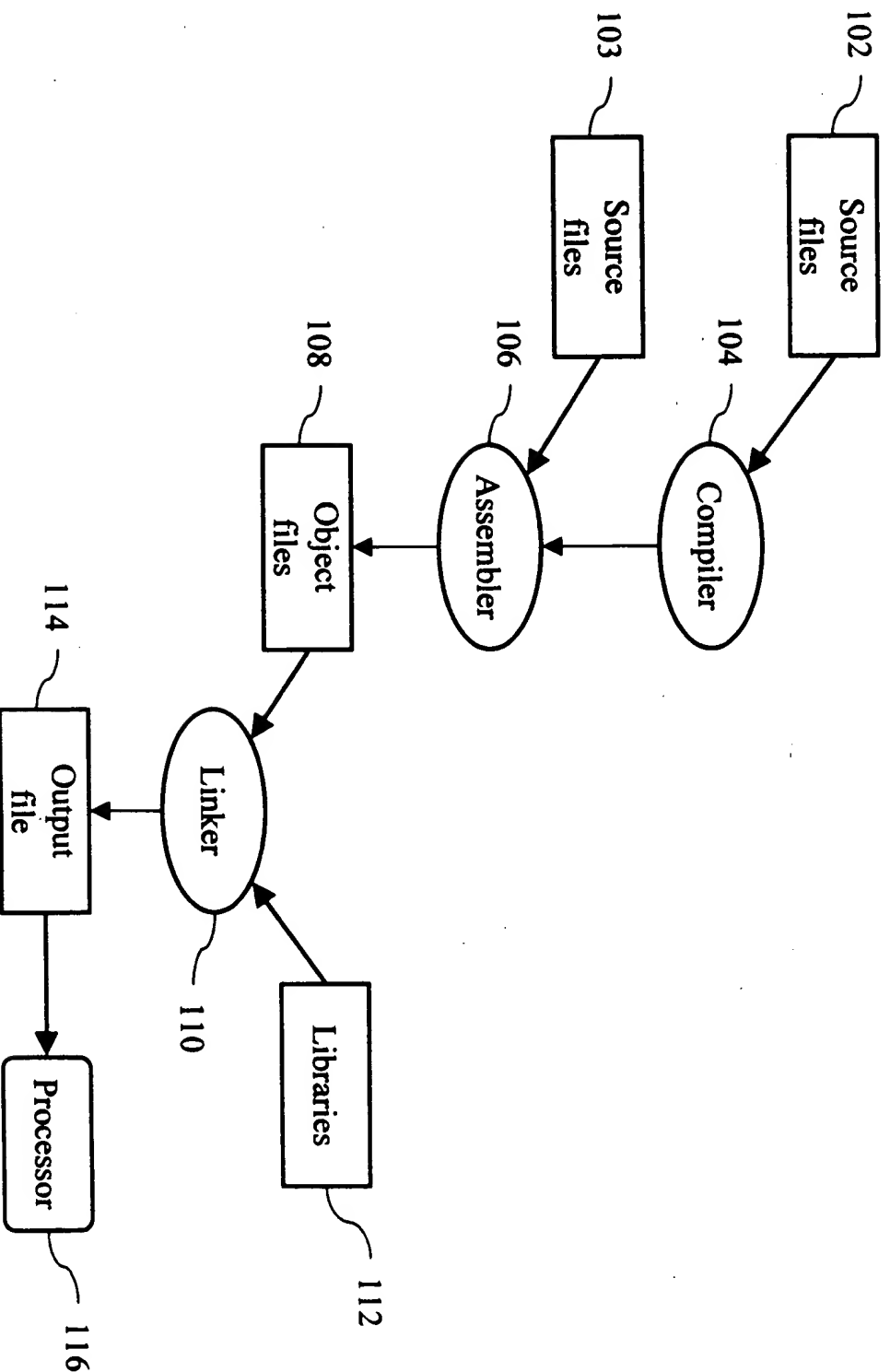


Figure 2A: Typical Linker Apparatus

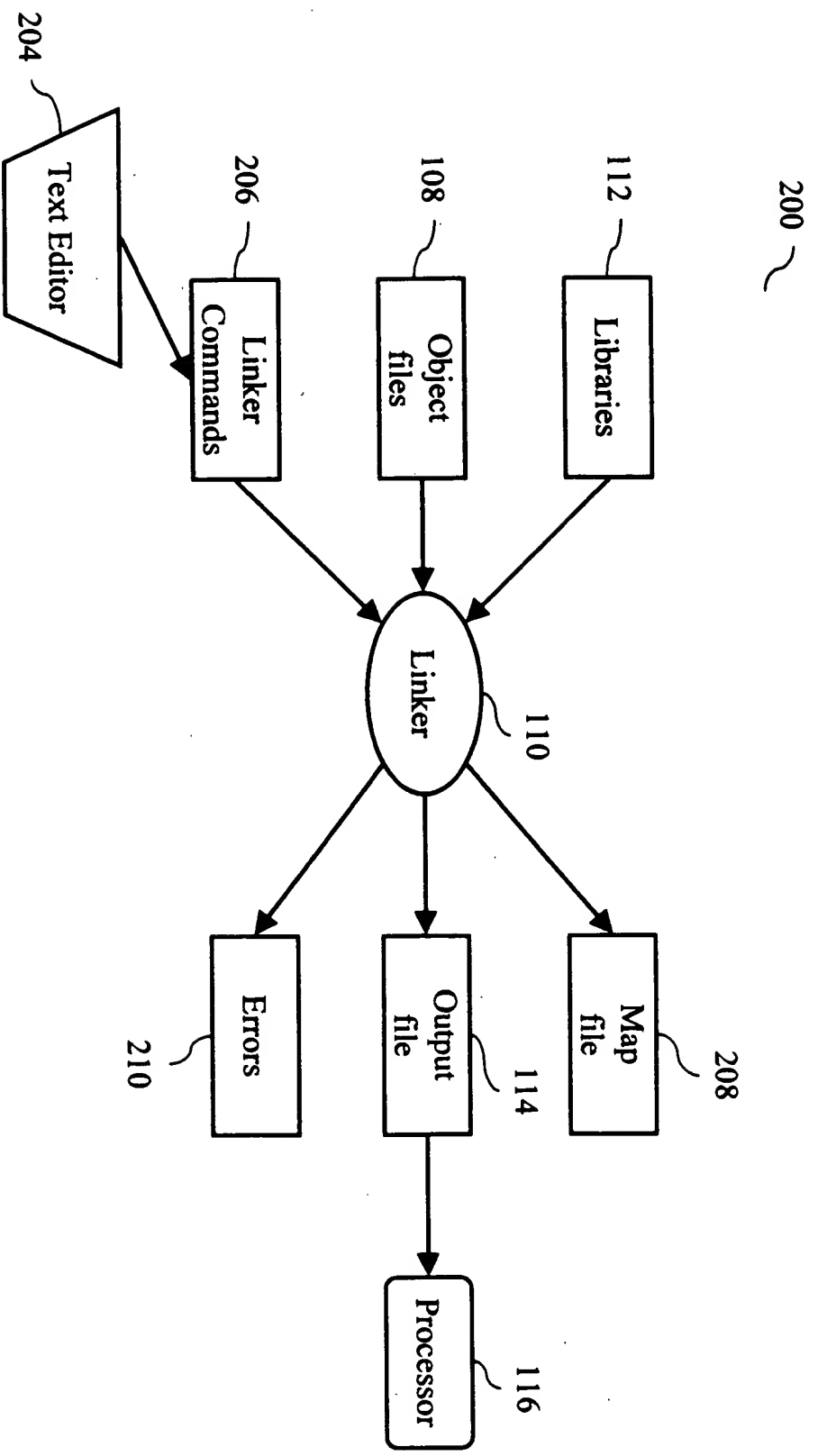


Figure 2B: Typical Linker Method

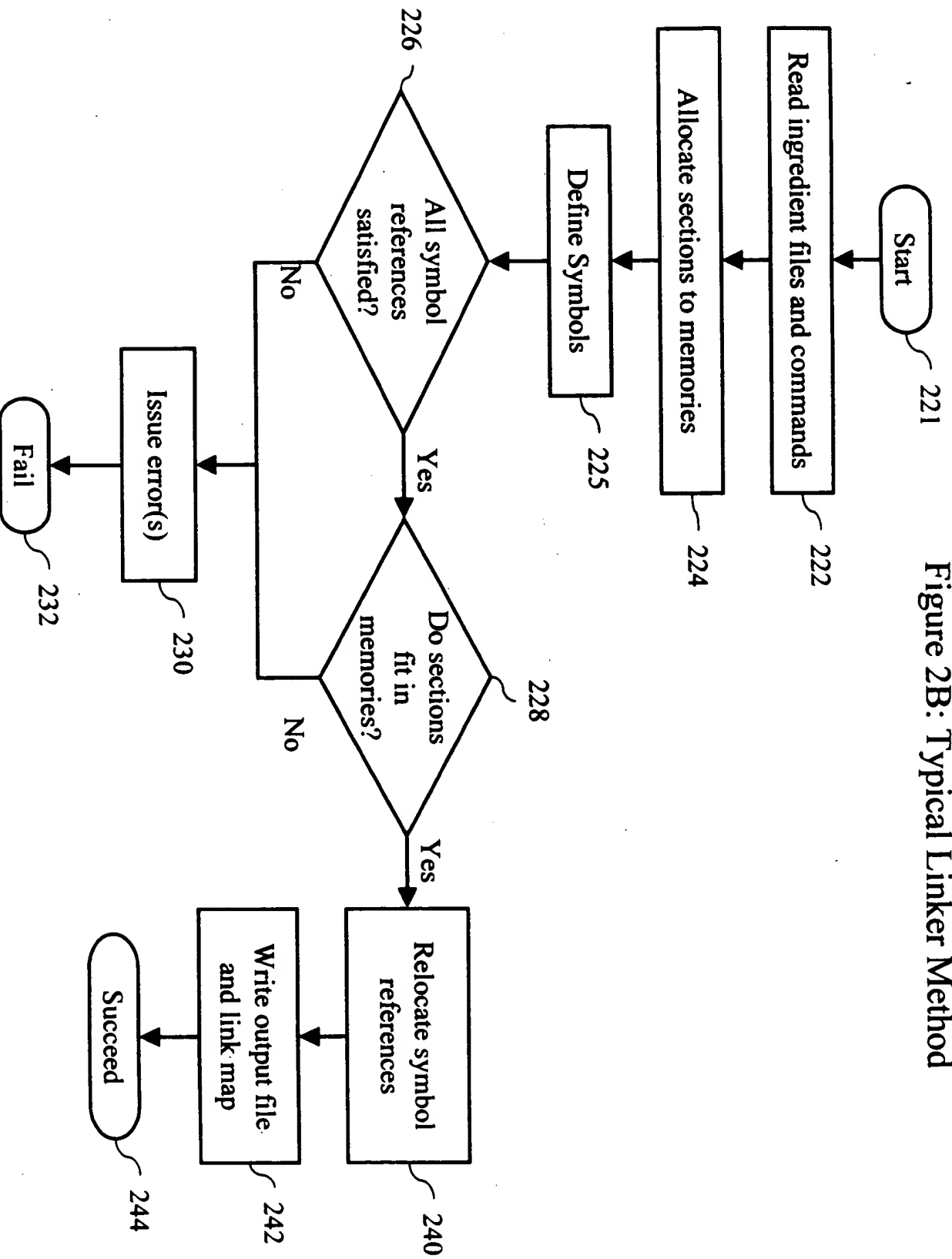


Figure 3: Linker Allocation and Output

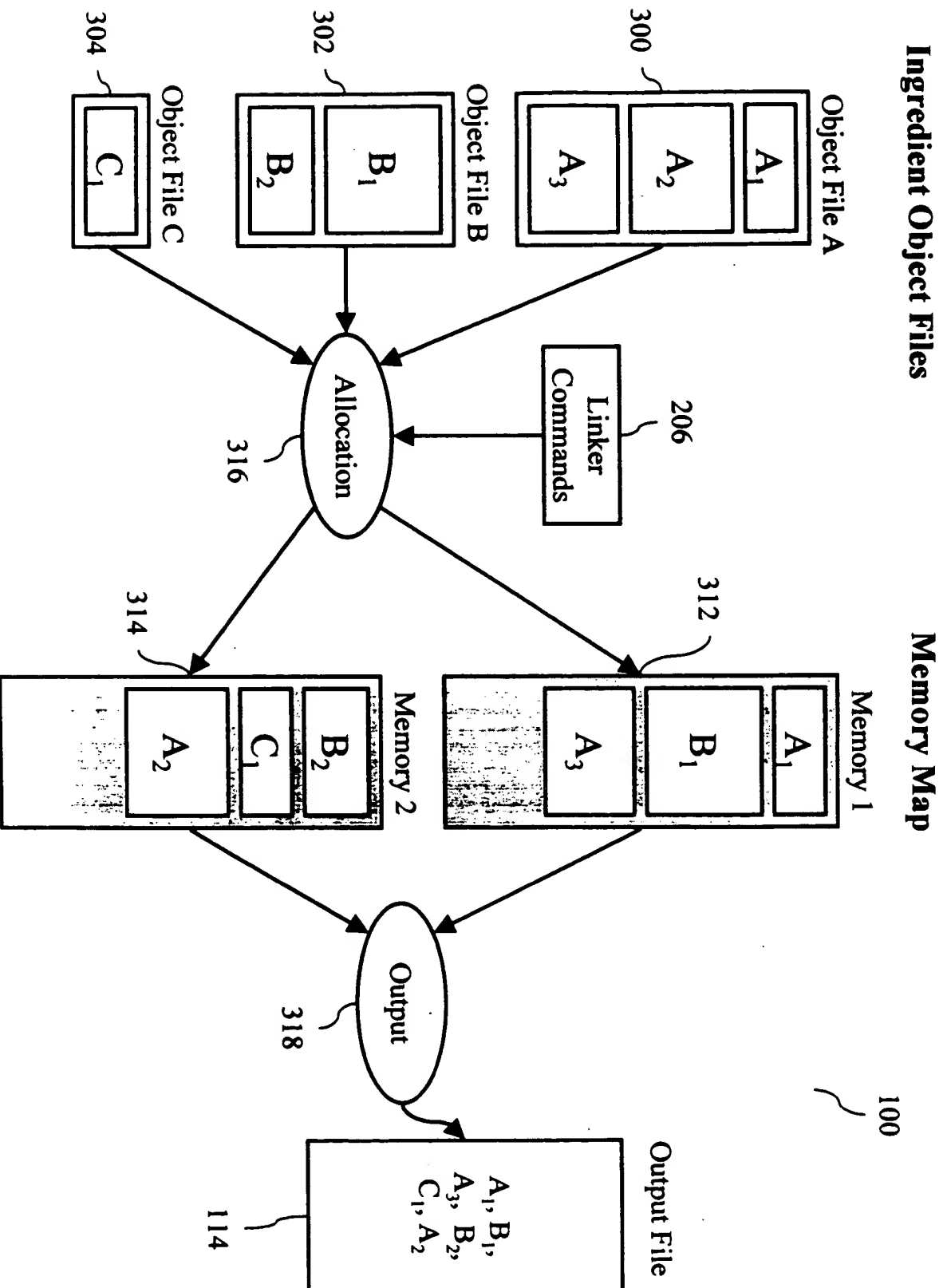


Figure 4: Linker Symbol Resolution

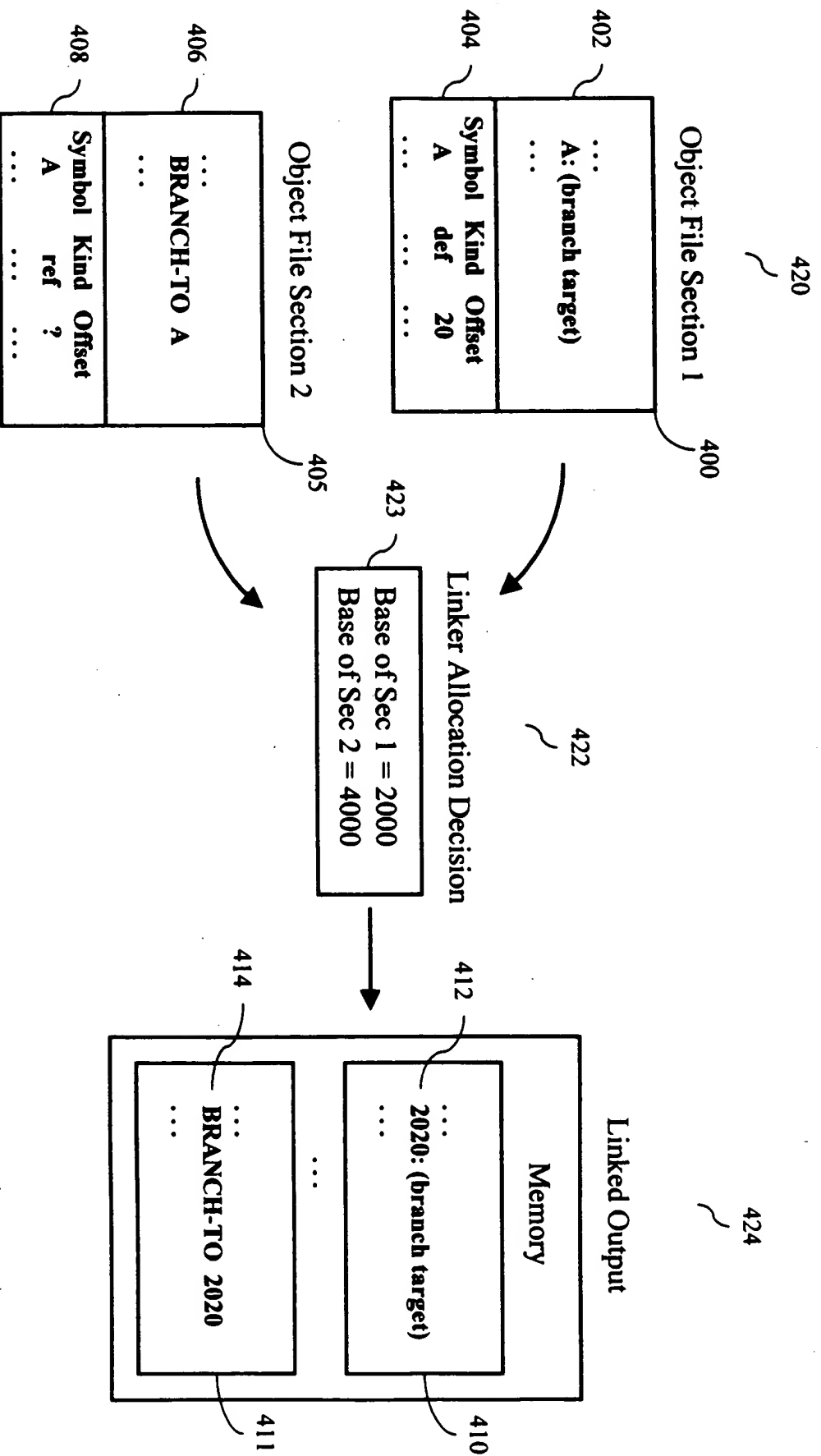


Figure 5A: Visual Linker Apparatus

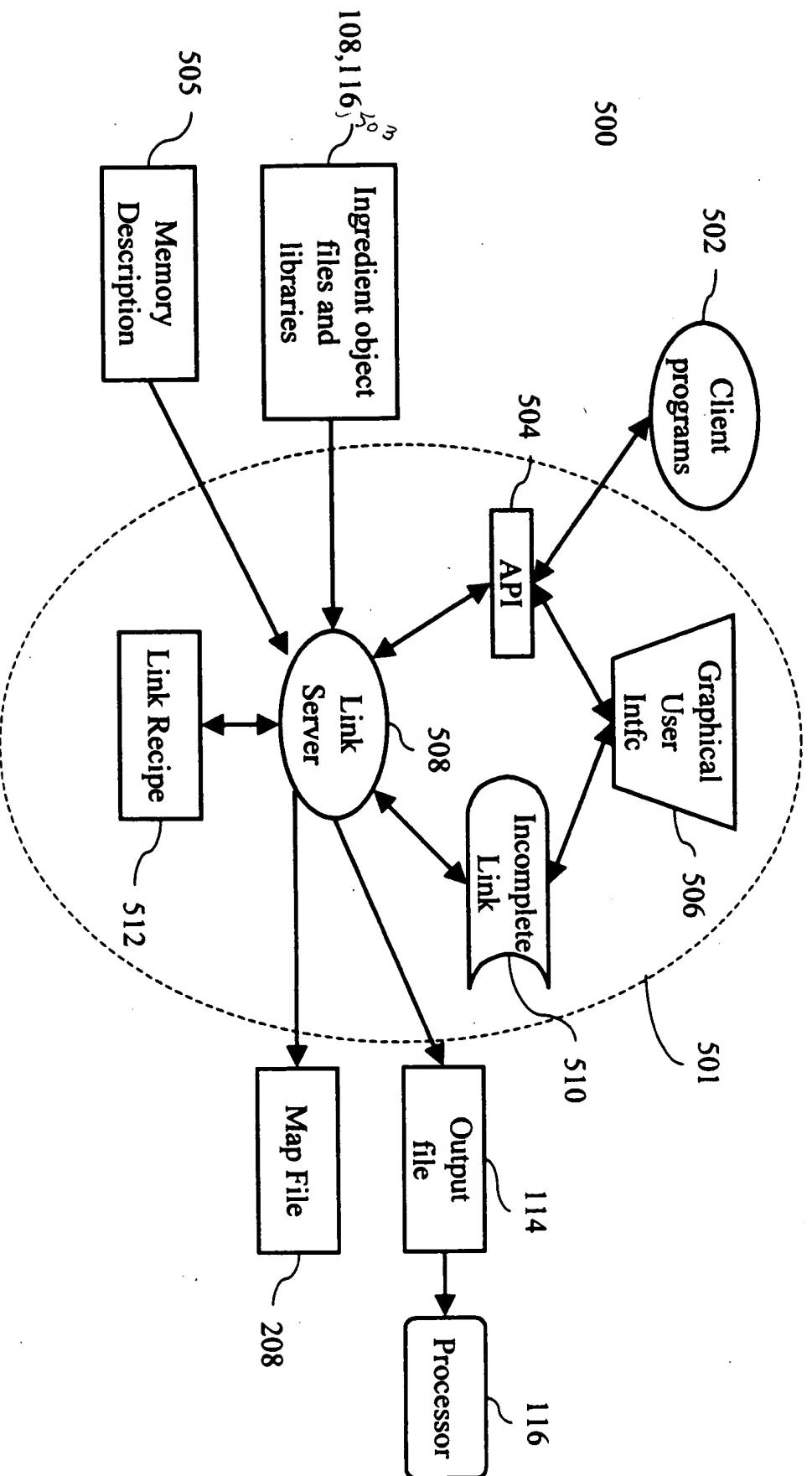
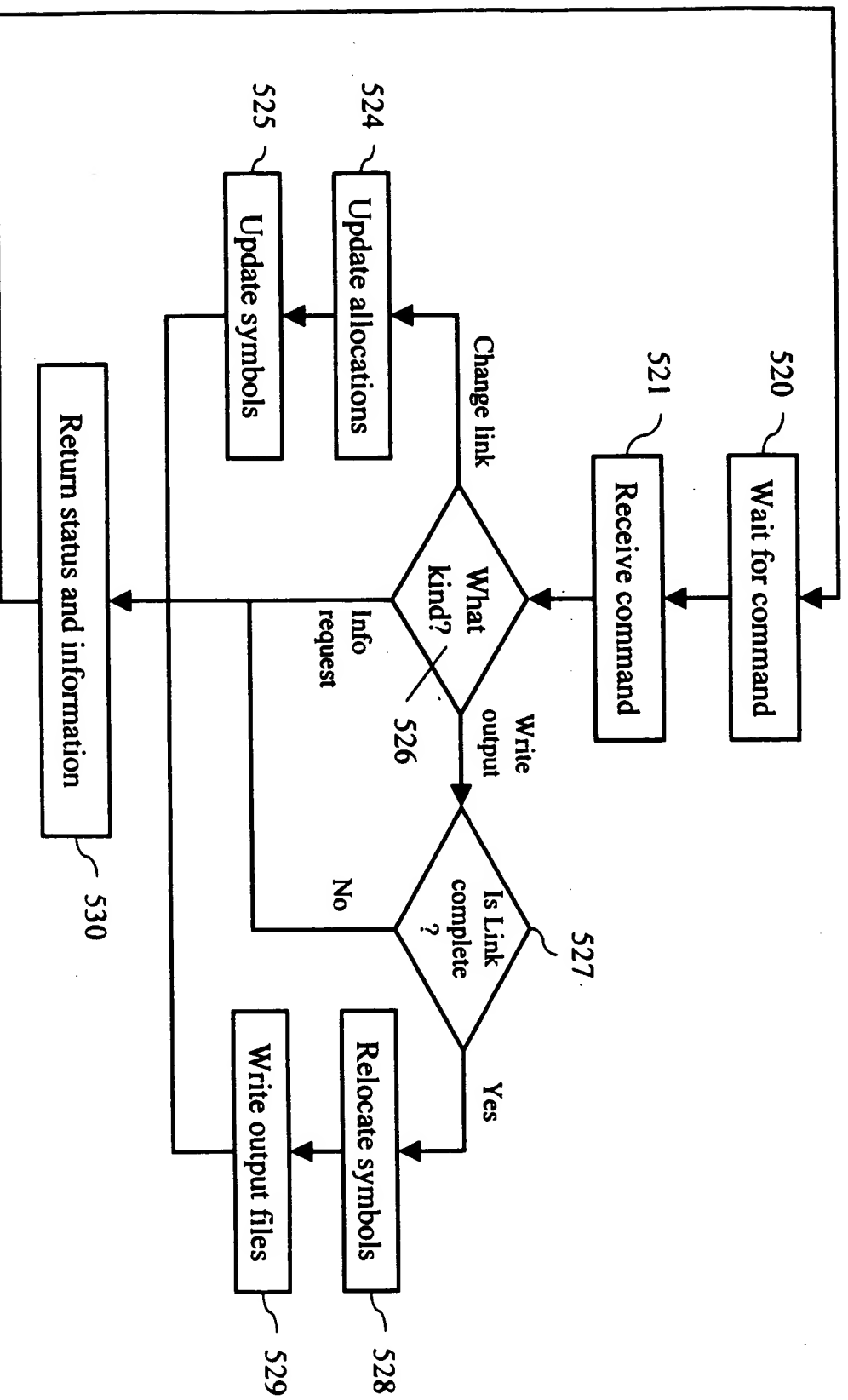


Figure 5B: Visual Linker Method







Bob

Memory Maps

EVMS320LCS49\_o.vy1

Link-Time Absolute Syms

run\_view

Not Yet Placed

Variables

Constants

data\_memory

data\_memory\_during\_boot

IO\_memory

IO\_memory\_during\_boot

program\_memory

RESERVED

DARAM

SARAM

code\_page\_0

code\_page\_0\_MP1

vectors

code\_page\_1

code\_page\_2

code\_page\_3

code\_page\_4

code\_page\_5

program\_memory\_when\_MP0

load\_view

Output Files

All Input Files

INFO begin parsing D:\src\src\bin\Debug\Templates\CS49\CS49\EVMS320LCS49\_o.vy1.mem  
INFO done parsing D:\src\src\bin\Debug\Templates\CS49\CS49\EVMS320LCS49\_o.vy1.mem  
INFO successfully completed parse of command line & files  
INFO concatenating all sections with base name: stack into a single output section of the same name  
INFO link-time symbol STACK\_SIZE defined  
INFO concatenating all sections with base name: cinit into a single output section of the same name  
INFO link-time symbol cinit defined  
INFO link-time symbol pinit defined

ns\boot\text	Code	0000000h	0000444h	000045h
ns\text\text	Code	0000000h	00003Ch	00003Ch
maintst\text	Code	0000000h	000015h	000016h
mantst\text	Code	0000000h	0002B5h	0002B6h
comonst\text	Code	0000000h	000843h	000844h
ns\ldv\text	Code	0000000h	000036h	000037h
ns\l_neg\text	Code	0000000h	000004h	000005h
ns\l_mul\text	Code	0000000h	000072h	000073h
ns\l_err\text	Code	0000000h	000099h	00009Ah
ns\l_div\text	Code	0000000h	00002Ah	00002Bh
ns\l_dv\text	Code	0000000h	00003Bh	00003Ch
ns\l_dv\text	Code	0000000h	00003Fh	000040h
ns\l_dv\text	Code	0000000h	00001Fh	000020h
ns\l_cmp\text	Code	0000000h	0000A5h	0000A7h
ns\l_log\text	Code	0000000h	000020h	000021h
ns\l_exp\text	Code	0000000h	000000h	000009h
ns\l_sub\text	Code	0000000h	00009Ah	00009Bh
ns\l_add\text	Code	0000000h	00000Fh	000010h
ns\l_str\text	Code	0000000h	00005Bh	00005Ch
ns\l_sqrt\text	Code	0000000h	00007Ch	00007Dh
ns\l_tan\text	Code	0000000h	00009Ch	00009Dh
ns\l_exp\text	Code	0000000h	00009Ch	00009Dh

run\_view\program\_memory

000100h

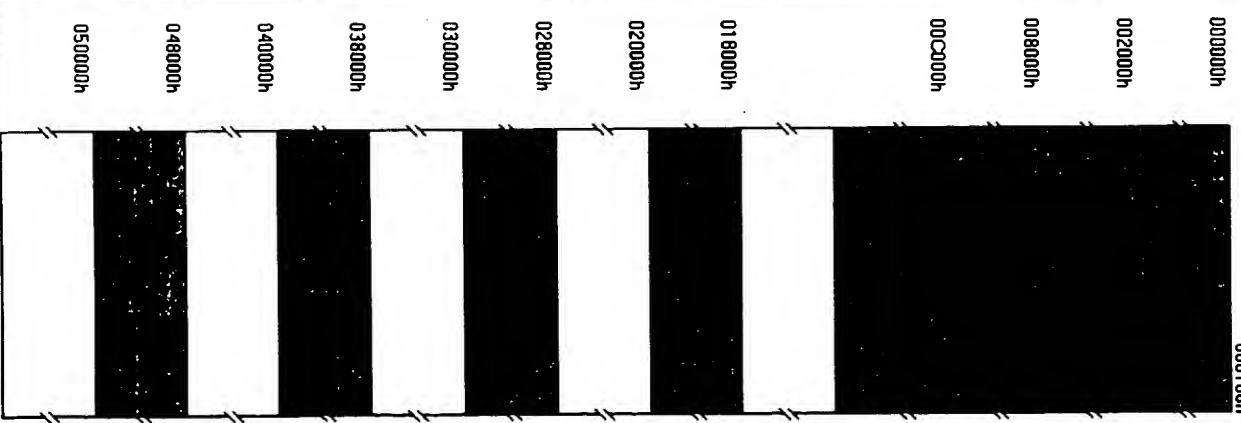


Fig. 2

Fig. 8

Link-Time Absolute Syms

load\_view

run\_view

data\_memory

regs

scratch

constants\_in\_scratch

DARAM

SARAM

external\_data\_RAM

data\_memory\_during\_boot

regs

scratch

rs\exp\const

rs\sqr\const

DARAM

bss

const

comentst\const

testst\const

rs\lenh\const

rs\log\const

rs\idexp\const

mainst\const

text

SARAM

FLASH

IO\_memory

IO\_memory\_during\_boot

program\_memory

RESERVED

DARAM

bss

const

text

SARAM

run\_view\data\_memory\scratch\constants\_in\_scratch

section rs\log\obj\const overflowed into run\_view\program\_memory\DARAM\const

could not resolve allocation for memory region run\_view\data\_memory\scratch because constraints of section

run\_view\data\_memory\scratch\constants\_in\_scratch could not be met with available free memory

unable to allocate section All Input Files\obj\rs\lib\idexp.obj\const into

run\_view\data\_memory\scratch\constants\_in\_scratch

section rs\lib\idexp.obj\const overflowed into run\_view\program\_memory\DARAM\const

could not resolve allocation for memory region run\_view\data\_memory\scratch because constraints of section

run\_view\data\_memory\scratch\constants\_in\_scratch could not be met with available free memory

unable to allocate section All Input Files\obj\rs\lib\idexp.obj\const into

run\_view\data\_memory\scratch\constants\_in\_scratch

section mainst\obj\const overflowed into run\_view\program\_memory\DARAM\const

run\_view\data\_memory

000100h

000000h

002000h

008000h

010000h

comentst\const

testst\const

rs\lenh\const

rs\log\const

rs\idexp\const

mainst\const

text

SARAM

FLASH

IO\_memory

IO\_memory\_during\_boot

program\_memory

RESERVED

DARAM

bss

const

text

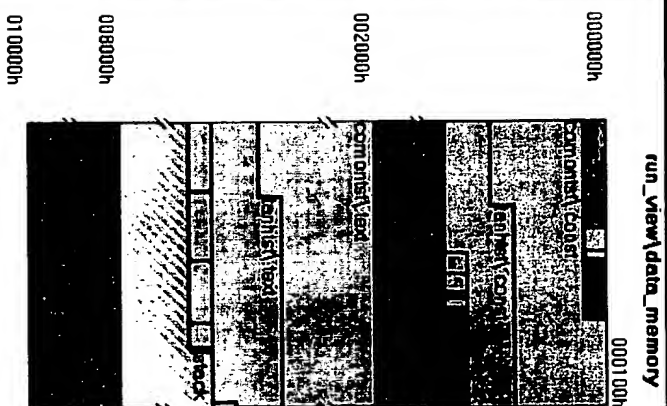
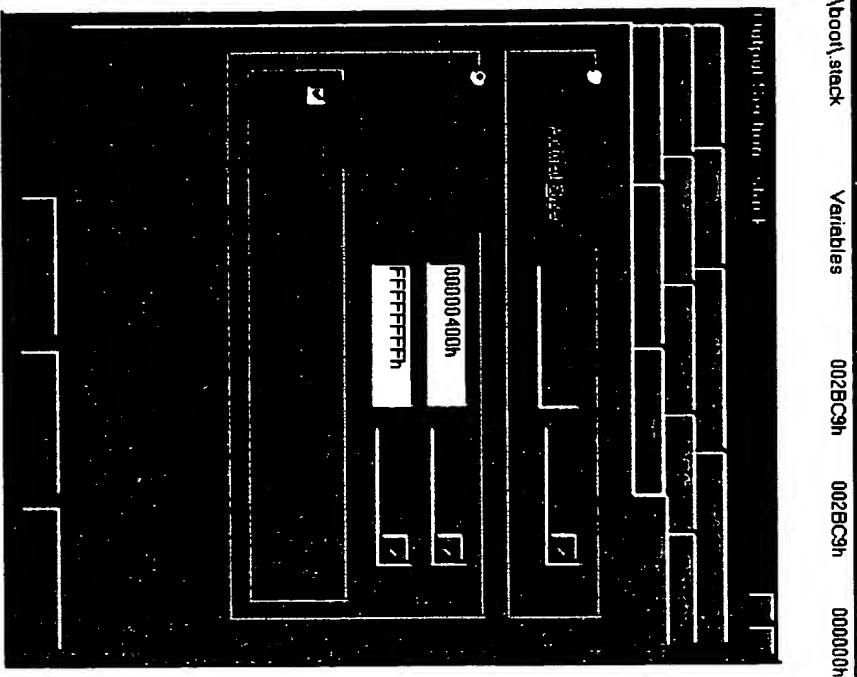
SARAM

Microsoft Visual Studio 6.0 SP4

```

i INFO run_view\data_memory\scratch\constants_in_scratch could not be met with available free memory
unable to allocate section All Input Files\obj\libs\lib\dxexp.obj\const into
run_view\data_memory\scratch\constants_in_scratch
i INFO section rs.lib\dxexp.obj\const overflowed into run_view\program_memory\DARAM\const
could not resolve allocation for memory region run_view\data_memory\scratch because constraints of section
run_view\data_memory\scratch\constants_in_scratch could not be met with available free memory
unable to allocate section All Input Files\obj\libs\main\obj\const into
run_view\data_memory\scratch\constants_in_scratch
i INFO section main\obj\const overflowed into run_view\program_memory\DARAM\const

```



```

CHANGE entry OF ts.lib\boot.obj\text_c_jind0 TO TRUE
NEW child output file OF Output Files WITH full_path= %a.out HOLDING map EVM320LCS49_gwv\
CHANGE out file name OF Output Files %a.out TO ".\bob.out"
CHANGE map file name OF Output Files bob.out TO ".\bob.map"
NEW child output section OF run_view\program_memory\SAFAM WITH name= const data_kind=constants
MOVE children of run_view\Not Yet Placed WITH kind= input section data_kind= constants TO run_view\program_memory\SAFAM\const
NEW child output section OF run_view\program_memory\DAFAM WITH name= text data_kind= code
MOVE objects reachable from ts.lib\sqrl.obj\text WITH kind= any section data_kind= code TO run_view\program_memory\DAFAM\text
END bob

```